PROCESS AND SKEW TOLERANT PRECHARGE CIRCUIT

ABSTRACT OF THE DISCLOSURE

A precharge circuit capable of generating a precharge pulse is provided. The precharge circuit includes circuitry for initiating charging of a precharge pulse at a first edge of a first clock-like signal. The precharge circuit also includes circuitry for ending the charging of the precharge pulse after a time period that is longer of a preset delay period and a time period designated by a second edge of the second clock-like signal. Methods for generating a precharge pulse are also included.